

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L20	1027	L19 and (CPU or processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/01 17:44
L28	0	("2005/0055683").URPN.	USPAT	OR	ON	2006/11/01 17:44
L30	1337	compar\$5 near flag	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/01 17:44
L31	7057	compar\$5 near5 flag	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/01 17:44
L32	873	(714/30).cccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/01 17:44
L33	501	(714/37).cccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/01 17:44
L34	1347	L32 or L33	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/01 17:44
L35	1027	L34 and (CPU or processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/01 17:44
L41	873	(714/30).cccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/01 17:44
L42	501	(714/37).cccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/01 17:44

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L43	1347	L41 or L42	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/01 17:44
L44	1027	L43 and (CPU or processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/01 17:44
L45	165	L44 and idle	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/01 17:44
L46	0	L44 and idle with process%3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/01 17:44
L54	9	L51 and flag	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/01 17:44
L57	0	float and L51	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/01 17:44

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processor diagnostic compiler

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[PDF] Design and Implementation of a Diagnostic Compiler for PL/I

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efficient **processor**. It effectively demonstrates that. compilers can provide better **diagnostic** ... **diagnostic compiler**. While programs are very likely to ...
portal.acm.org/citation.cfm?doid=361972.361992

Design of a separable transition-diagram compiler

Richard W. Conway , Thomas R. Wilcox, Design and implementation of a **diagnostic compiler** for PL/I, Communications of the ACM, v.16 n.3, p.169-179, ...
portal.acm.org/citation.cfm?coll=GUIDE&dl=GUIDE&id=366704

[PDF] The Design and Implementation of a Table Driven, Interactive ...

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CAPS is a highly interactive **diagnostic compiler**. interpreter that allows beginning programmers to pre- pare, debug, and execute fairly simple programs at ...
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[PDF] Configuration, Control, and Diagnostic Subsystem - Reliability and ...

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ly to add a **compiler** heuristic tuned for **diagnostic** generation. ... Driven **Processor** for the Jellybean Machine at MIT Artificial Intelligence. Laboratory. ...
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the Verilog or VHDL source RTL for the **processor**, the hardware test-bench, complete **diagnostics**, including coverage of TIE extensions, and scripts for ...

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